

FIG. 3B  
(Prior Art)

FIG. 3A  
(Prior Art)

3/8

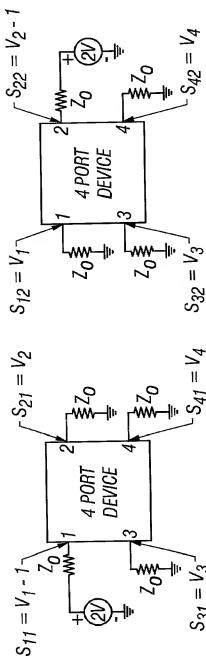


FIG. 5A

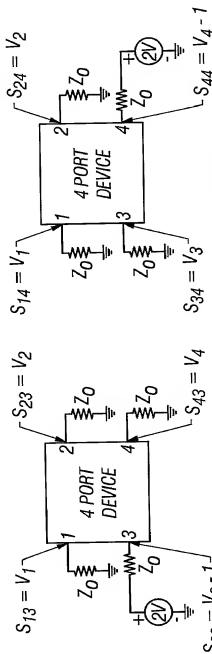


FIG. 5B

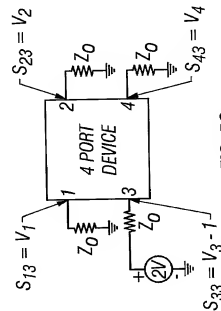


FIG. 5C

$$[S] = \begin{bmatrix} S_{11} & S_{12} & S_{13} & S_{14} \\ S_{21} & S_{22} & S_{23} & S_{24} \\ S_{31} & S_{32} & S_{33} & S_{34} \\ S_{41} & S_{42} & S_{43} & S_{44} \end{bmatrix}$$

FIG. 5E

4/8

601

-
S.P.I.T. PROCESS STATUS
•
□

CHECKED PORT INTEGRITY

FEEDING PORT 1  
FEEDING PORT 2  
FEEDING PORT 3  
FEEDING PORT 4  
FEEDING PORT 5  
FEEDING PORT 6

BUILDING \*tdp AND \*.fdp FILES  
BUILDING UNTITLED.ssub  
PROCESS COMPLETE

602

603

-
SCATTERING PARAMETER INTEGRATION TOOL (S.P.I.T.) - VERSION 1.00
•
□

SPICE SUBCIRCUIT (sub)		PLUTO 2		READ FILE	
$Z_0$		50		UNTITLED	
$F_{max}$ (MHZ)		12732.395447351		100	
FREQUENCY STEP (MHZ)		5		200	
SUBCIRCUIT LABELS		%c in	%v in	%c out	%v out
PORT DESIGNATION		1	2	3	4
				5	6
DONE BUILDING CITI FILE					
BUILD S-PARAMETERS					
EXIT					

FIG. 6

5/8

701 SCATTERING PARAMETER INTEGRATION TOOL (S.P.I.T.)- VERSION 1.00

702 SPICE SUBCIRCUIT (.sub) [PLUTO 2] 709 READ FILE

703  $Z_0$  [50] 710 UNTITLED

704  $F_{max}$  (MHZ) [12732.395447351] 711 100

705 FREQUENCY STEP (MHZ) [5] 712 SIMULATION DURATION (ns) [200]

706 SUBCIRCUIT LABELS

707 PORT DESIGNATION

708 BUILD S-PARAMETERS

713 EXIT

714

FIG. 7

SCATTERING PARAMETER INTEGRATION TOOL (S.P.I.T.)- VERSION 1.00

SPICE SUBCIRCUIT (.sub) [PLUTO 2] READ FILE

$Z_0$  [50] UNTITLED

$F_{max}$  (MHZ) [12732.395447351] 100

FREQUENCY STEP (MHZ) [5] SIMULATION DURATION (ns) [200]

SUBCIRCUIT LABELS

PORT DESIGNATION

%c in	%v in	%p in	%c out	%v out	%p out
1	2	3	4	5	6

BUILD S-PARAMETERS

EXIT

FIG. 8

6/8

SCATTERING PARAMETER INTEGRATION TOOL (S.P.I.T.)- VERSION 1.00													•	<input type="checkbox"/>
SPICE SUBCIRCUIT (sub)			PLUTO 2			READ FILE								
$Z_0$			50			OUTPUT FILE (.ssub)			UNTITLED					
$F_{max}$ (MHz)			12732.395447351			MINIMUM RISE TIME (ps)			100					
FREQUENCY STEP (MHz)			5			SIMULATION DURATION (ns)			200					
SUBCIRCUIT LABELS			%c	in	%v	in	%p	in	%c	out	%v	out	%p	out
PORT DESIGNATION			1	2	3	4	5	6						
DONE BUILDING CITI FILE													713	
BUILD S-PARAMETERS														
EXIT														

FIG. 9

8/8

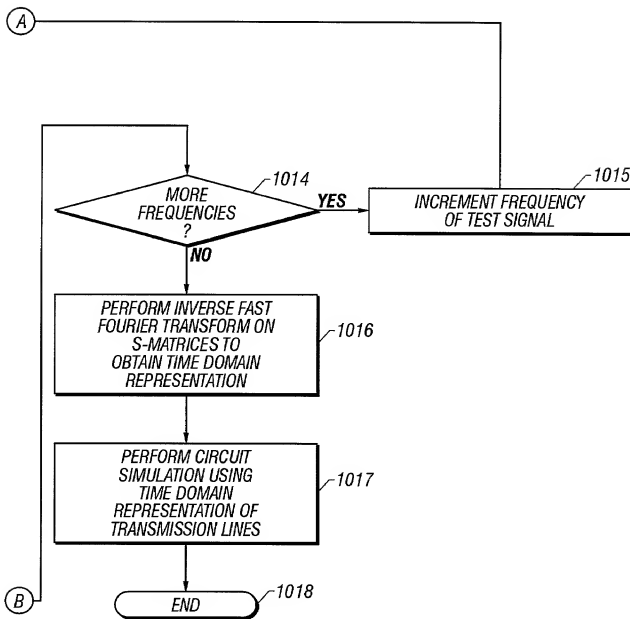


FIG. 10B

DECLARATION AND POWER OF ATTORNEY  
FOR PATENT APPLICATIONATTORNEY DOCKET NO. 10004730-1

As a below named inventor, I hereby declare that:

My residence/post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**Software Tools For Generation Of Scattering Parameter Models Of N-Port Lumped Element Circuits For Use In Spice Simulators**

the specification of which is attached hereto unless the following box is checked:

( ) was filed on \_\_\_\_\_ as US Application No. or PCT International Application  
Number \_\_\_\_\_ and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understood the contents of the above-identified specification, including the claims, as amended by any amendment(s) referred to above. I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR 1.56.

**Foreign Application(s) and/or Claim of Foreign Priority**

I hereby claim foreign priority benefits under Title 35, United States Code Section 119 of any foreign application(s) for patent or inventor(s) certificate listed below and have also identified below any foreign application for patent or inventor(s) certificate having a filing date before that of the application on which priority is claimed:

COUNTRY	APPLICATION NUMBER	DATE FILED	PRIORITY CLAIMED UNDER 35 U.S.C. 119
N/A			YES: _____ NO: _____
			YES: _____ NO: _____

**Provisional Application**

I hereby claim the benefit under Title 35, United States Code Section 119(e) of any United States provisional application(s) listed below:

APPLICATION NUMBER	FILING DATE

**U. S. Priority Claim**

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

APPLICATION NUMBER	FILING DATE	STATUS (patented/pending/abandoned)

**POWER OF ATTORNEY:**

As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

Customer Number 022879Place Customer  
Number Bar Code  
Label hereSend Correspondence to:  
**HEWLETT-PACKARD COMPANY**  
Intellectual Property Administration  
P.O. Box 272400  
Fort Collins, Colorado 80527-2400**Direct Telephone Calls To:****William J. Streeter**  
**970 898 3886**

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full Name of Inventor: Karl J. BojsCitizenship: USResidence: 1419 Stonehenge Drive Ft. Collins, CO 80525Post Office Address: Same as ResidenceInventor's Signature Karl J. BojsDate 07/03/2001